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PATENT
8013-1147

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Hirokazu HONDA

Conf. 7187

Application No. 09/678,609

Group 2827

Filed October 4, 2000

Examiner D. Graybill

MULTILAYER INTERCONNECTION BOARD, SEMICONDUCTOR DEVICE HAVING
THE SAME, AND METHOD OF FORMING THE SAME AS WELL AS METHOD OF
MOUNTING THE SEMICONDUCTOR CHIP ON THE INTERCONNECTION BOARD

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 15, 2004

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying document, a copy of which is attached to this statement, is made of record on the enclosed Form PTO-1449.

A concise explanation of the relevance of this item is that this reference was cited by the Japanese Patent Office in the corresponding Japan Application Serial No. 2000-057767, filed March 2, 2000. A copy of the Japanese Official Action in which it was cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. An English translation of the enclosed portion is also attached hereto.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this Statement.

Respectfully submitted,

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September 15, 2004

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use several sheets if necessary)

.98(b)

ATTY. DOCKET NO.:
8013-1147

SERIAL NO.
09/678,609

APPLICANT
Hirokazu HONDA

FILING DATE
October 4, 2000

GROUP
2827

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | | PATENT NUMBER | ISSUE DATE | PATENTEE | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE |
|---------------------|----|---------------|---------------|----------|-------|--------------|-------------------------------|
| | AA | | | | | | |
| | AB | | | | | | |
| | AC | | | | | | |
| | AD | | | | | | |

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

| | | DOCUMENT NO. | PUBL. DATE | COUNTRY OR PATENT OFFICE | CLASS | SUB CLASS | TRANSLATION YES NO |
|--|----|--------------|---------------|--------------------------|-------|--------------|-----------------------|
| | AI | 10-125818 | 05/98 | Japan | | | |
| | AJ | | | | | | |
| | AK | | | | | | |
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| | AO | | | | | | |
| | AP | | | | | | |
| | AQ | | | | | | |

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

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| | AT | |
| | AU | |
| | AV | |
| | AX | |
| | AY | |

EXAMINER

DATE CONSIDERED

EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.